

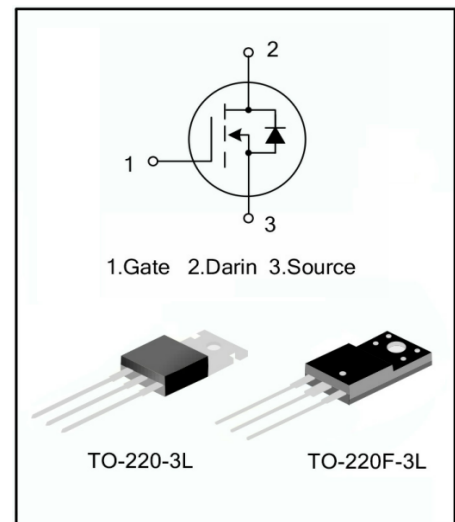
600V N-Channel MOSFET

GENERAL DESCRIPTION

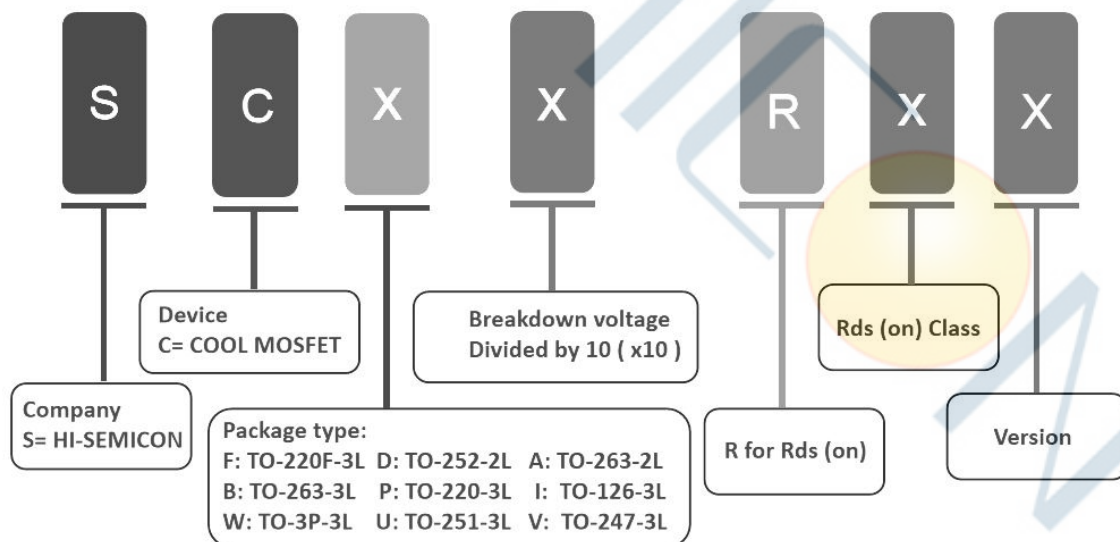
SJ-FET is new generation of high voltage MOSFET family that is utilizing an advanced charge balance mechanism for outstanding low on-resistance and lower gate charge performance. This advanced technology has been tailored to minimize conduction loss, provide superior switching performance, and withstand extreme dv/dt rate and higher avalanche energy. SJ-FET is suitable for various AC/DC power conversion in switching mode operation for higher efficiency.

FEATURES

- ◆ New revolutionary high voltage technology
- ◆ Typ. $R_{DS(on)} = 0.14\Omega$
- ◆ Ultra Low Gate Charge (typ. $Q_g = 49nC$)
- ◆ 100% avalanche tested



NOMENCLATURE



ORDERING INFORMATION

Part No.	Package	Marking	Material	Packing
SCF60R160C	TO-220F-3L	SCF60R160C	Pb free	Tube
SCP60R160C	TO-220-3L	SCP60R160C	Pb free	Tube

ABSOLUTE MAXIMUM RATINGS

Symbol	Parameter	SCF60R160C	SCP60R160C	Unit
VDSS	Drain-Source Voltage	600		V
ID	Drain Current-Continuous (TC = 25°C)	24		A
	-Continuous (TC = 100°C)	15		
IDM	Drain Current - Pulsed	96		A
VGSS	Gate-Source voltage	±30		V
EAS	Single Pulsed Avalanche Energy (Note1)	1062		mJ
dv/dt	Reverse diode dv/dt (Note2)	15		V/ns
dv/dt	MOSFET dv/dt ruggedness (Note3)	50		V/ns
PD	Power Dissipation (TC = 25°C)	47	208	W
TJ	Operation Junction Temperature Range	-55 to +150		°C
TSTG	Storage Temperature Range	-55 to +150		°C
TL	Maximum Lead Temperature for Soldering Purpose, 1/8" from Case for 5 Seconds	300		°C

Thermal Characteristics

Symbol	Parameter	SCF60R160C	SCP60R160C	Unit
RθJC	Thermal Resistance, Junction-to-Case	2.66	0.6	°C/W
RθJA	Thermal Resistance, Junction-to-Ambient	62.5	62.5	°C/W

Electrical Characteristics TC = 25°C unless otherwise noted

Symbol	Parameter	Conditions	Min	Typ	Max	Unit
Off Characteristics						
BV _{DSS}	Drain-Source Breakdown Voltage	V _{GS} = 0V, I _D = 250μA,	600	-	-	V
I _{DSS}	Zero Gate Voltage Drain Current	V _{DS} = 600V, V _{GS} = 0V	-	-	1	μA
I _{GSS}	Gate-Body Leakage Current, Forward	V _{GS} = ±30V, V _{DS} = 0V	-	-	±100	nA
V _{GS(th)}	Gate Threshold Voltage	V _{DS} = V _{GS} , I _D = 250μA	2.0	-	4.0	V
R _{DS(on)}	Static Drain-Source On-Resistance	V _{GS} = 10V, I _D = 10A	-	0.14	0.16	Ω
Dynamic Characteristics						
C _{iss}	Input Capacitance	V _{DS} = 100V, V _{GS} = 0V,	-	1480	-	pF
C _{oss}	Output Capacitance	f = 1.0MHz	-	84	-	pF
C _{rss}	Reverse Transfer Capacitance		-	4.8	-	pF
Switching Characteristics						
t _{d(on)}	Turn-On Delay Time	V _{DD} = 300V, I _D =20A, V _{GS} = 10V R _G =25Ω (Note 4,5)	-	21	-	ns
t _r	Turn-On Rise Time		-	74	-	ns
t _{d(off)}	Turn-Off Delay Time		-	213	-	ns
t _f	Turn-Off Fall Time		-	65	-	ns
Q _g	Total Gate Charge	V _{DS} = 480V, I _D =20.0A, V _{GS} =10V	-	49	-	nC
Q _{gs}	Gate-Source Charge	(Note 4,5)	-	12	-	nC
Q _{gd}	Gate-Drain Charge		-	25	-	nC
Drain-Source Diode Characteristics and Maximum Ratings						
I _S	Maximum Continuous Drain-Source Diode Forward Current		-	-	24	A
I _{SM}	Maximum Pulsed Drain-Source Diode Forward Current		-	-	96	A
V _{SD}	Drain-Source Diode Forward Voltage	V _{GS} = 0V, I _S = 5.0A	-	-	1.4	V
t _{rr}	Reverse Recovery Time	V _{GS} = 0V, I _S = 5.0A, dI _F /dt=100A/μs	-	442	-	ns
Q _{rr}	Reverse Recovery Charge		-	7.0	-	μC

Notes:

- 1.L=79mH, I_{AS}=4.8A, V_{DD}=100V, R_G=25Ω, starting temperature T_J=25°C;
- 2.DS=0~400V, I_{SD}<=24A, T_J=25°C;
- 3.V_{DS}=0~480V;
4. Pulse Test: Pulse width ≤300μs, Duty cycle≤2%;
5. Essentially independent of operating temperature.

Typical Performance Characteristics

Figure 1. On-Region Characteristics

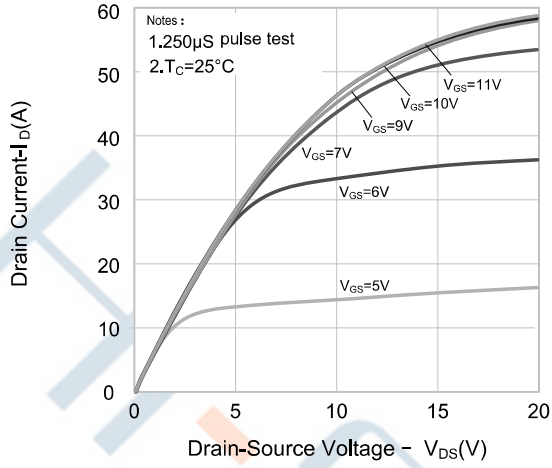


Figure 2. Transfer Characteristics

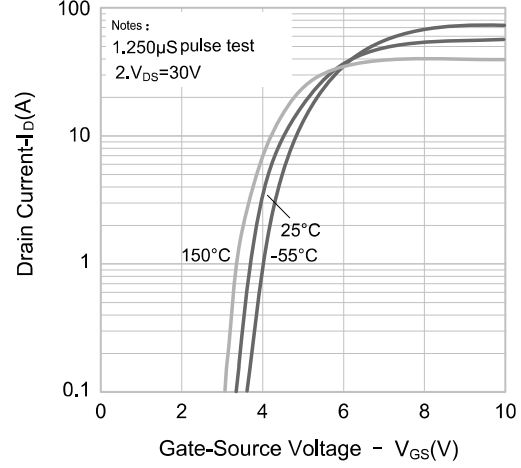


Figure 3. On-Resistance Variation vs. Drain-Current, Gate Voltage

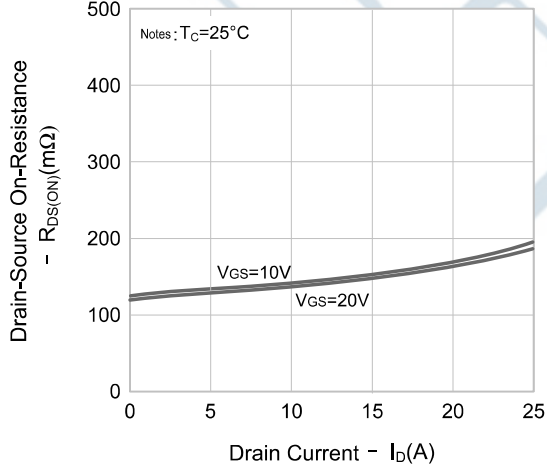


Figure 4. Body Diode Forward Voltage Variation vs. Source Current and Temperature

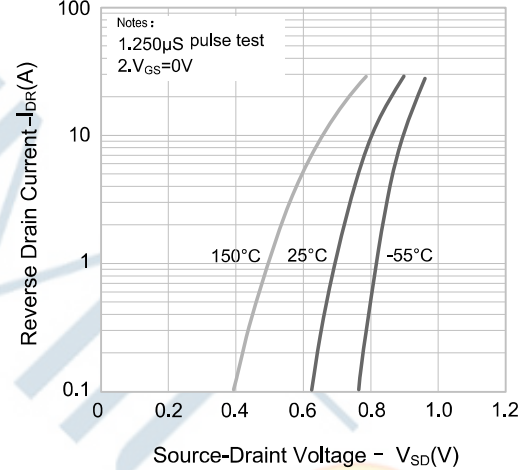


Figure 5. Capacitance Characteristics

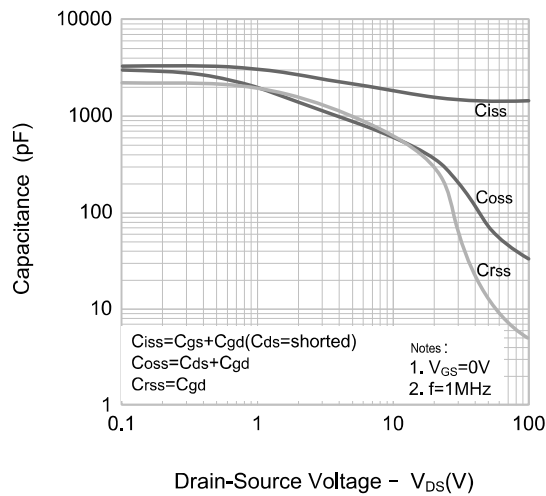
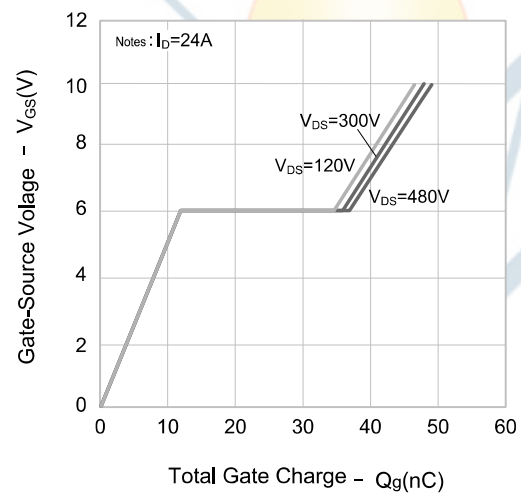


Figure 6. Gate Charge Characteristics



Typical Performance Characteristics

Figure 7. Breakdown Voltage Variation vs. Temperature

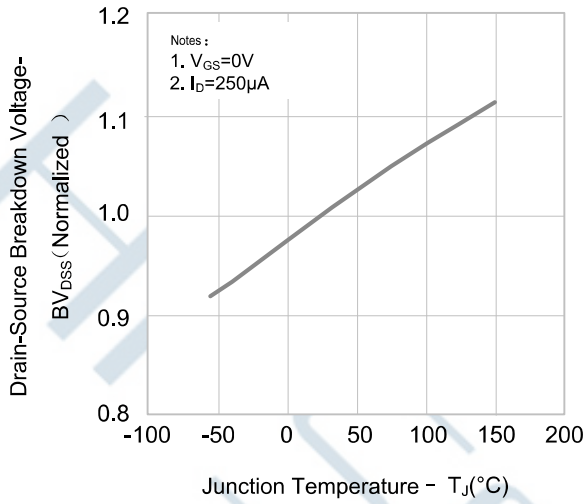


Figure 8. On-resistance Variation vs. Temperature

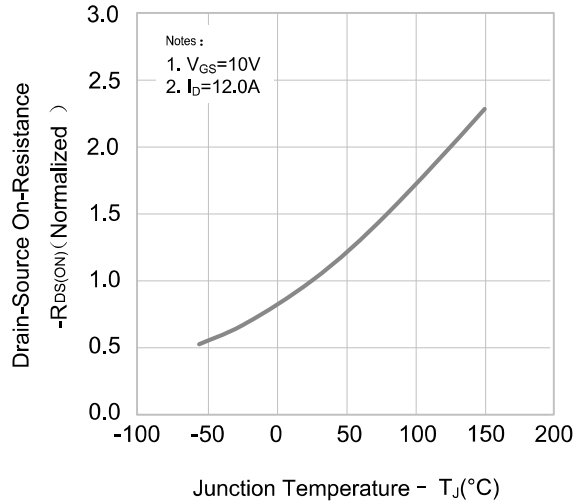


Figure 9-1. Max. Safe Operating Area (SCF60R160C)

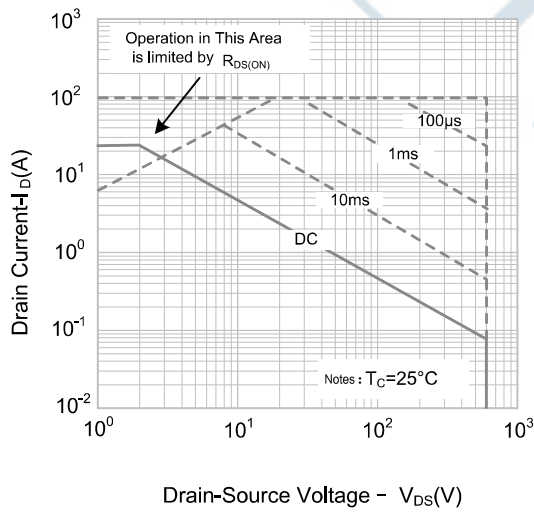
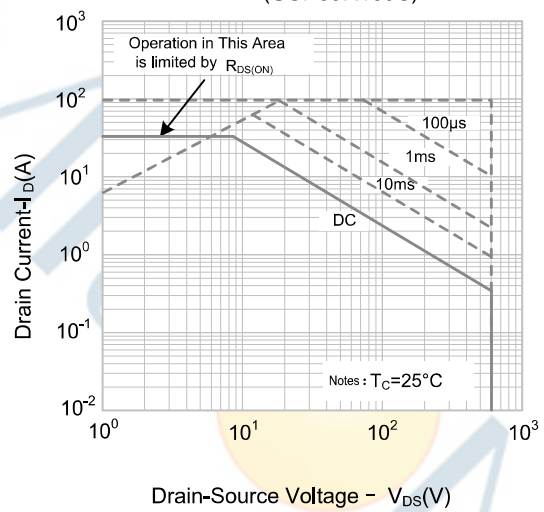
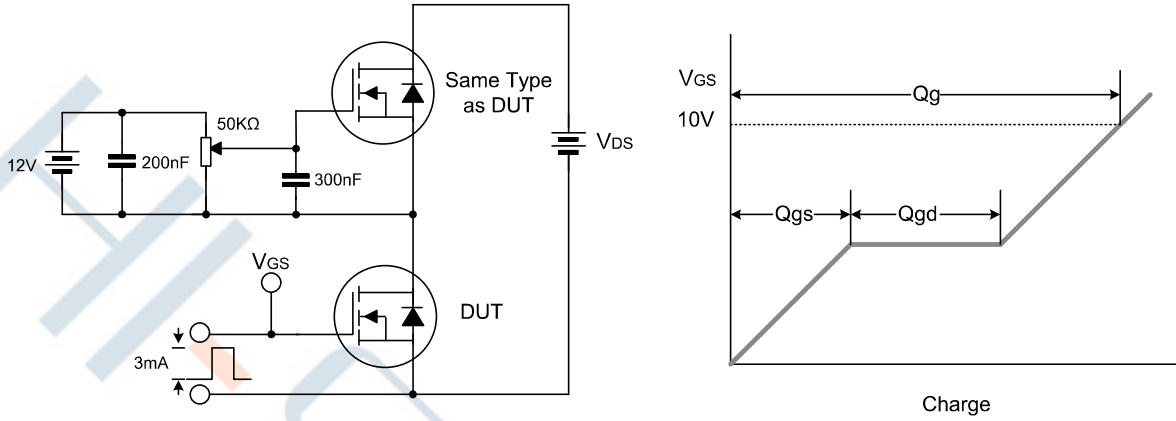


Figure 9-2. Max. Safe Operating Area (SCP60R160C)

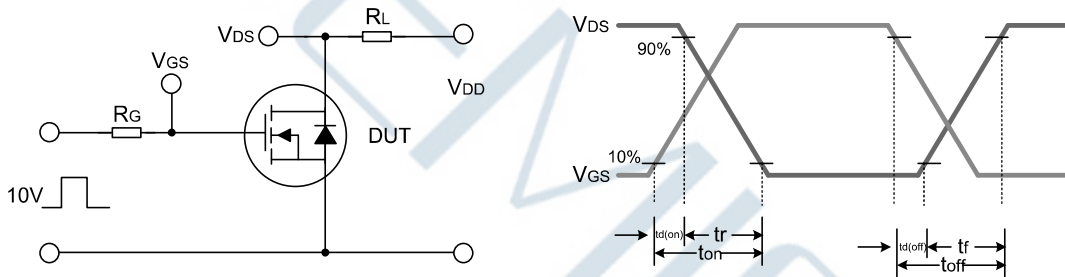


Test circuits

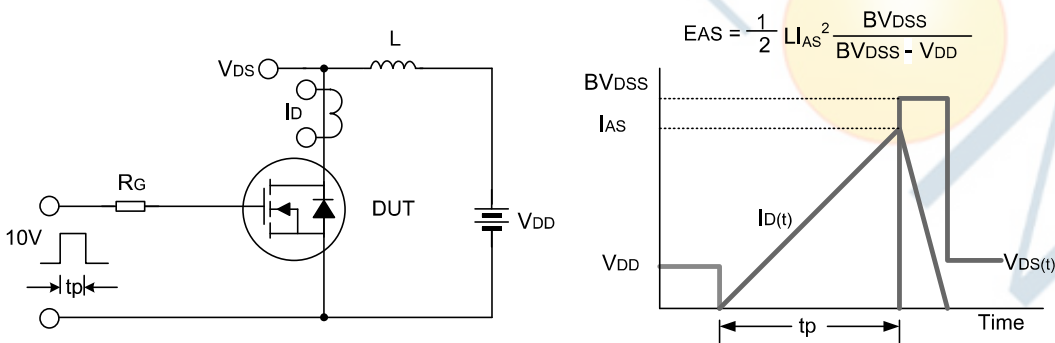
Gate Charge Test Circuit & Waveform



Resistive Switching Test Circuit & Waveform

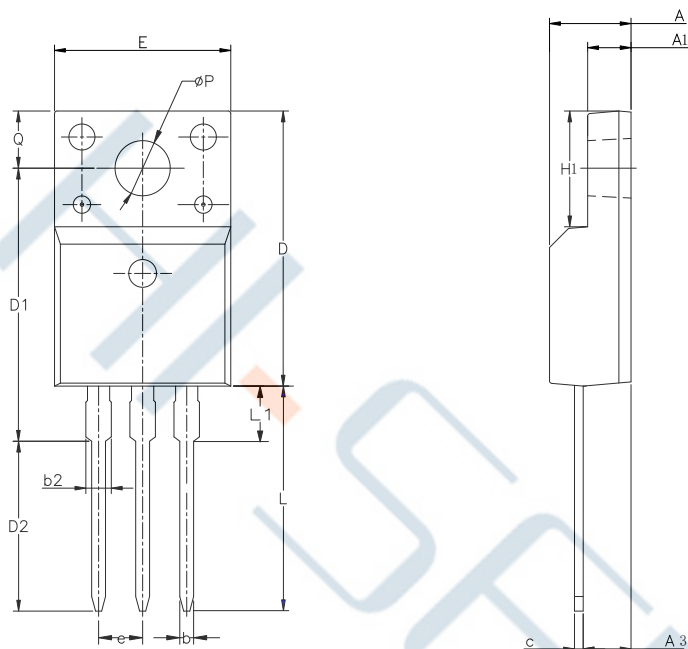


Unclamped Inductive Switching Test Circuit & Waveform



TO-220F-3L

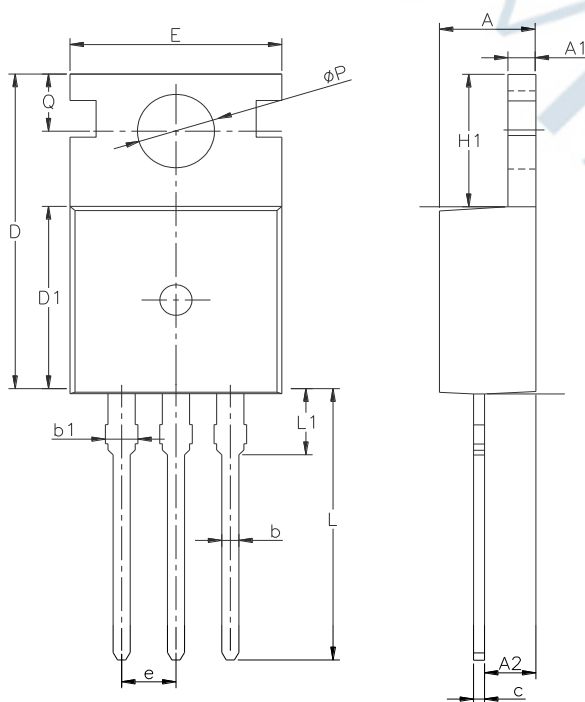
Unit: mm



SYMBOL	MIN	NOM	MAX
A	4.42	4.70	5.02
A1	2.30	2.54	2.80
A3	2.50	2.76	3.10
b	0.70	0.80	0.90
b2	—	—	1.47
c	0.35	0.50	0.65
D	15.25	15.87	16.25
D1	15.30	15.75	16.30
D2	9.30	9.80	10.30
E	9.73	10.16	10.36
e	2.54BCS		
H1	6.40	6.68	7.00
L	12.48	12.98	13.48
L1	/	/	3.50
ϕP	3.00	3.18	3.40
Q	3.05	3.30	3.55

TO-220-3L

Unit: mm



SYMBOL	MIN	NOM	MAX
A	4.30	4.50	4.70
A1	1.00	1.30	1.50
A2	1.80	2.40	2.80
b	0.60	0.80	1.00
b1	1.00	—	1.60
c	0.30	—	0.70
D	15.10	15.70	16.10
D1	8.10	9.20	10.00
E	9.60	9.90	10.40
e	2.54BSC		
H1	6.10	6.50	7.00
L	12.60	13.08	13.60
L1	—	—	3.95
ϕP	3.40	3.70	3.90
Q	2.60	—	3.20