

N-Ch and P-Ch Power MOSFET

GENERAL DESCRIPTION

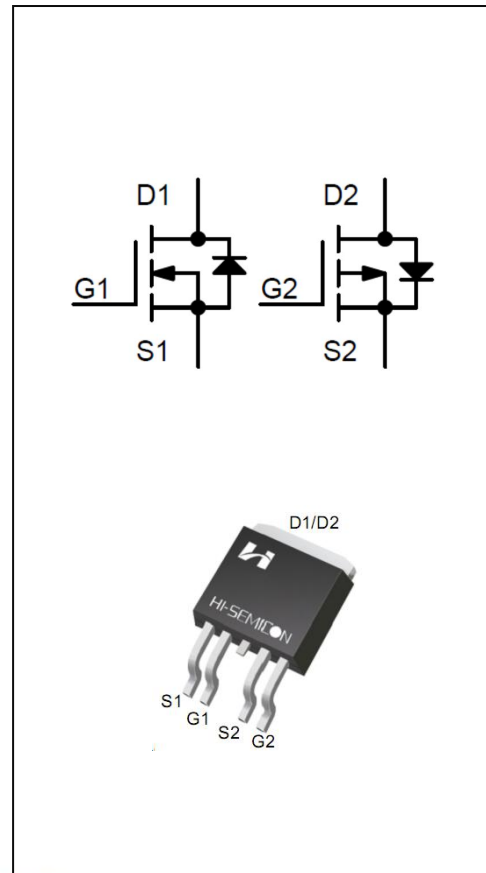
Complementary Enhancement MOSFET in a TO-252-4L Package. The SFQ0320T4 uses advanced trench technology and design to provide excellent $R_{DS(on)}$ with low gate charge can be used in a wide variety of applications.

Features

- ◆ N-CHANNEL
 - $V_{DS}=30V, I_D=25A$
 - $R_{DS(on)(TYP)}=15.3m\Omega; (V_{GS}=10V, I_D=15A)$
 - $R_{DS(on)(TYP)}=21.1m\Omega; (V_{GS}=4.5V, I_D=10A)$
- ◆ P-CHANNEL
 - $V_{DS}=-30V, I_D=-24A$
 - $R_{DS(on)(TYP)}=12.3m\Omega; (V_{GS}=-10V, I_D=-5A)$
 - $R_{DS(on)(TYP)}=16.5m\Omega; (V_{GS}=-4.5V, I_D=-5A)$

Applications

- ◆ Power factor correction (PFC)
- ◆ Switched mode power supplies (SMPS)
- ◆ Uninterruptible power supply (UPS)
- ◆ LED lighting power



ORDERING INFORMATION

Part No.	Package	Marking	Material	Packing
SFQ0320T4	T0-252-4L	SFQ0320T4	Pb Free	Reel

ABSOLUTE MAXIMUM RATINGS (T_J=25°C unless otherwise noted)

Characteristics		Symbol	N-CHANNEL	P-CHANNEL	Unit
Drain-Source Voltage		V _{DS}	30	-30	V
Gate-Source Voltage		V _{GS}	±20	±20	V
Drain Current	T _C = 25°C	I _D	25	-24	A
	T _C = 100°C		20	-19	
Drain Current Pulsed(Note 1)		I _{DM}	87.5	-84	A
Power Dissipation(T _C =25°C)		P _D	20		W
Operation Junction Temperature Range		T _J	-55 to +150		°C
Storage Temperature Range		T _{stg}	-55 to +150		°C
Maximum lead temperature for soldering purposes, 1/8" from case for 5 seconds		TL	300		°C

THERMAL CHARACTERISTICS

Characteristics	Symbol	MAX	Unit
Thermal Resistance, Junction-to-Case	R _{θJC}	3.5	°C/W
Thermal Resistance, Junction-to-Ambient	R _{θJA}	62.5	°C/W

N-Ch ELECTRICAL CHARACTERISTICS

Characteristics	Symbol	Test conditions	Min.	Typ.	Max.	Unit
Off Characteristics						
Drain -Source Breakdown Voltage	B _{VDS}	V _{GS} =0V, I _D =250μA	30	35	--	V
Drain-Source Leakage Current	I _{DSS}	V _{DS} =30V, V _{GS} =0V	--	1.9	100	nA
Gate-Source Leakage Current	I _{GSS}	V _{GS} =20V, V _{DS} =0V	--	5.2	100	nA
Gate-Source Leakage Current	I _{GSS}	V _{GS} =-20V, V _{DS} =0V	--	-3.5	-100	nA
On Characteristics						
Gate Threshold Voltage	V _{GS(th)}	V _{GS} = V _{DS} , I _D =250μA	1	1.6	2.5	V
Static Drain- Source On State Resistance	R _{DS(on)}	V _{GS} =10V, I _D =15A	--	15.3	20	mΩ
		V _{GS} =4.5V, I _D =10A	--	21.1	28	mΩ
Forward Transconductance	g _{FS}	V _{DS} =10V, I _D =5A	5	8.9	15	S
Dynamic Characteristics						
Input Capacitance	C _{iss}	V _{DS} =15V V _{GS} =0V f=1.0MHZ	--	1540	--	pF
Output Capacitance	C _{oss}		--	197	--	
Reverse Transfer Capacitance	C _{rss}		--	133	--	
Switching Characteristics						
Turn-on Delay Time	t _{d(on)}	V _{DD} =20V; V _{GS} =4.5V R _G =3Ω I _D =15A (Note 2.3)	--	8.3	--	nS
Turn-on Rise Time	t _r		--	68	--	
Turn-off Delay Time	t _{d(off)}		--	132	--	
Turn-off Fall Time	t _f		--	76	--	
Total Gate Charge	Q _g	V _{DS} =20V, I _D =15A V _{GS} =4.5V (Note 2.3)	--	27	--	nC
Gate-Source Charge	Q _{gs}		--	6.8	--	
Gate-Drain Charge	Q _{gd}		--	7.2	--	

SOURCE-DRAIN DIODE RATINGS AND CHARACTERISTICS

Characteristics	Symbol	Test conditions	Min.	Typ.	Max.	Unit
Continuous Source Current	I_S	Integral Reverse P-N Junction Diode in the MOSFET	--	--	25	A
Pulsed Source Current	I_{SM}		--	--	87.5	
Diode Forward Voltage	V_{SD}	$I_S=15A, V_{GS}=0V$	--	0.89	1.4	V
Reverse Recovery Time	T_{rr}	$I=8A, V_{GS}=0V,$ $dI/dt=100A/\mu S(\text{Note 2})$	--	8.5	--	ns
Reverse Recovery Charge	Q_{rr}		--	12.1	--	μC

NOTE:

- 1.Pulse width limited by maximum junction temperature
- 2.Pulse Test: Pulse width $\leq 300\mu s$, Duty cycle $\leq 2\%$
- 3.Essentially independent of operating temperature

P-Ch ELECTRICAL CHARACTERISTICS

Characteristics	Symbol	Test conditions	Min.	Typ.	Max.	Unit
Off Characteristics						
Drain -Source Breakdown Voltage	B_{VDSS}	$V_{GS}=0V, I_D=250\mu A$	-30	-36	--	V
Drain-Source Leakage Current	I_{DSS}	$V_{DS}=30V, V_{GS}=0V$	--	-3.1	100	nA
Gate-Source Leakage Current	I_{GSS}	$V_{GS}=20V, V_{DS}=0V$	--	5.6	100	nA
Gate-Source Leakage Current	I_{GSS}	$V_{GS}=-20V, V_{DS}=0V$	--	-6.4	-100	nA
On Characteristics						
Gate Threshold Voltage	$V_{GS(th)}$	$V_{GS}=V_{DS}, I_D=-250\mu A$	-1.0	-1.5	-2.0	V
Static Drain- Source On State Resistance	$R_{DS(on)}$	$V_{GS}=-10V, I_D=-10A$	--	12.3	16.0	$m\Omega$
		$V_{GS}=-4.5V, I_D=-5A$	--	16.5	21.0	$m\Omega$
Forward Transconductance	g_{FS}	$V_{DS}=-5V, I_D=-10A$	10	15	20	/
Dynamic Characteristics						
Input Capacitance	C_{iss}	$V_{DS}=-15V$ $V_{GS}=0V$ $f=1.0MHz$	--	1520	--	pF
Output Capacitance	C_{oss}		--	221	--	
Reverse Transfer Capacitance	C_{rss}		--	121	--	
Switching Characteristics						
Turn-on Delay Time	$t_{d(on)}$	$V_{DD}=-15V; V_{GS}=-10V$ $R_G=3.0\Omega, I_D=-15A$ (Note 2.3)	--	9.3	--	nS
Turn-on Rise Time	t_r		--	10.4	--	
Turn-off Delay Time	$t_{d(off)}$		--	21	--	
Turn-off Fall Time	t_f		--	12.4	--	
Total Gate Charge	Q_g	$V_{DS}=-15V, I_D=-15A$ $V_{GS}=-10V$ (Note 2.3)	--	29.7	--	nC
Gate-Source Charge	Q_{gs}		--	6.8	--	
Gate-Drain Charge	Q_{gd}		--	7.6	--	

SOURCE-DRAIN DIODE RATINGS AND CHARACTERISTICS

Characteristics	Symbol	Test conditions	Min.	Typ.	Max.	Unit
Continuous Source Current	I_S	Integral Reverse P-N Junction Diode in the MOSFET	--	--	-24	A
Pulsed Source Current	I_{SM}		--	--	-84	
Diode Forward Voltage	V_{SD}	$I_S = -10A, V_{GS} = 0V$	--	-0.87	-1.4	V
Reverse Recovery Time	T_{rr}	$I = -15A, V_{GS} = 0V,$ $dI/dt = 100A/\mu S$ (Note 2)	--	23	--	ns
Reverse Recovery Charge	Q_{rr}		--	8.6	--	μC

NOTE:

1. Pulse width limited by maximum junction temperature
2. Pulse Test: Pulse width $\leq 300\mu s$, Duty cycle $\leq 2\%$
3. Essentially independent of operating temperature

N-Channel Typical Performance Characteristics

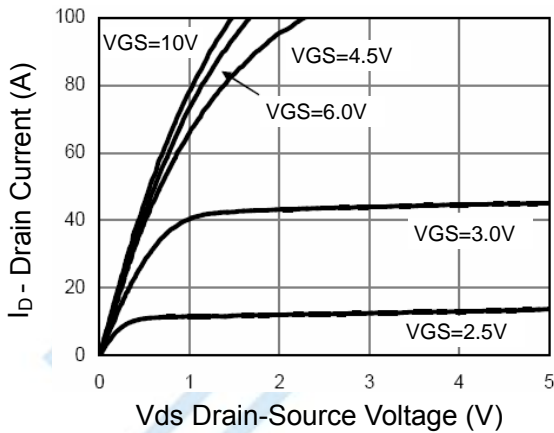


Figure 1 Output Characteristics

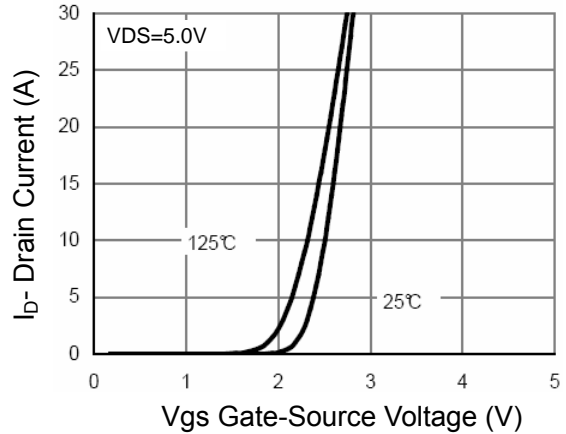


Figure 2 Transfer Characteristics

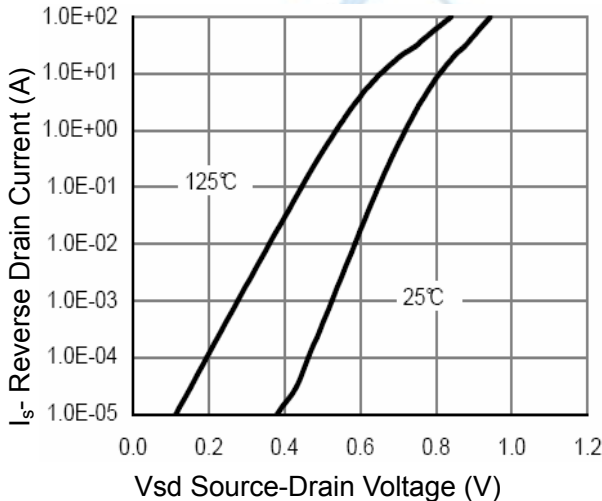


Figure 3 Source- Drain Diode Forward

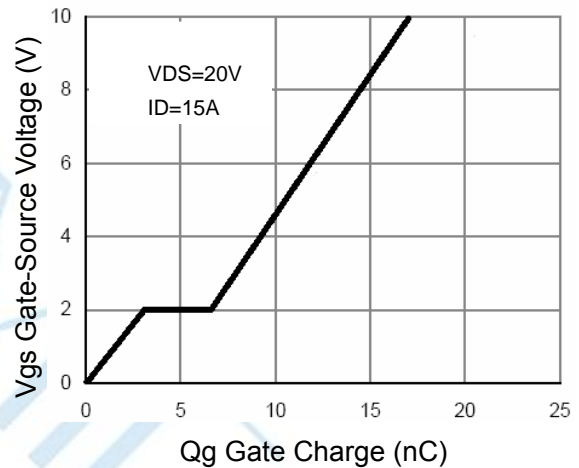


Figure 4 Gate Charge

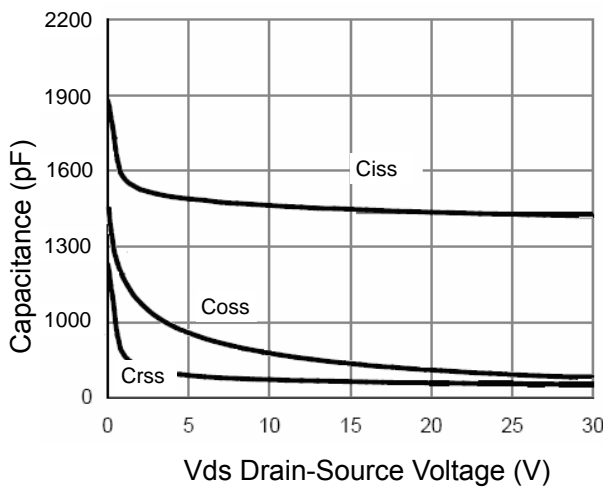


Figure 5 Capacitance vs Vds

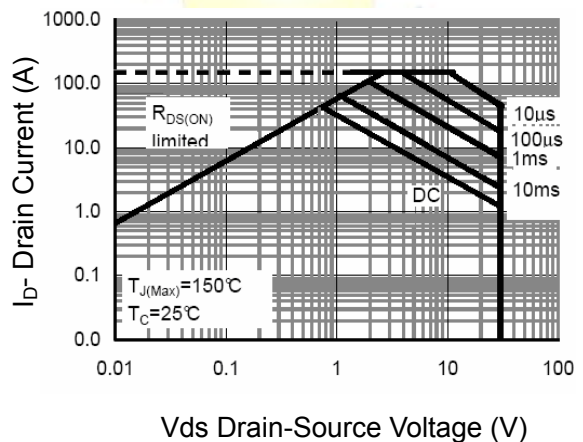


Figure 6 Safe Operation Area

N-Channel Typical Performance Characteristics

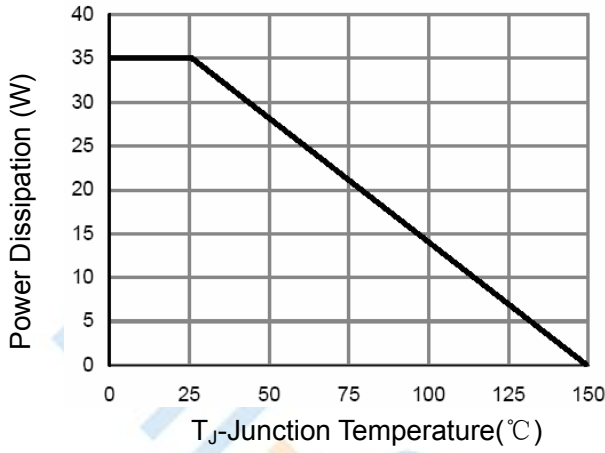


Figure 7 Power De-rating

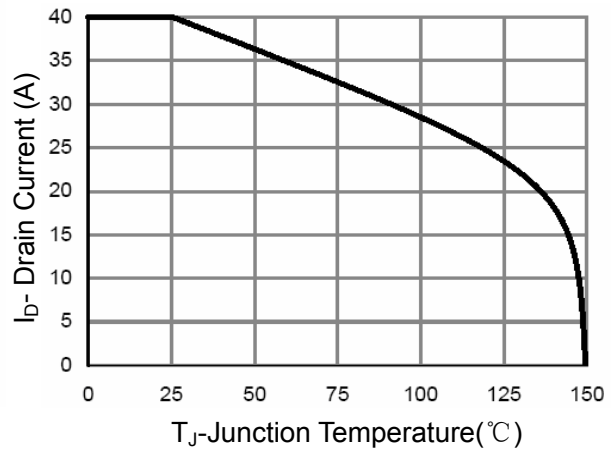


Figure 8 I_D Current De-rating

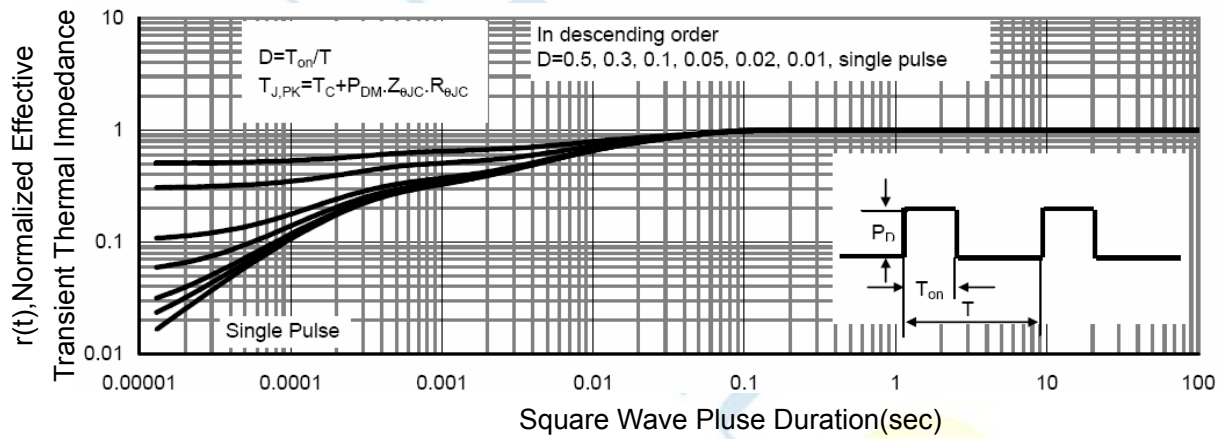


Figure 9 Normalized Maximum Transient Thermal Impedance

P-Channel Typical Performance Characteristics

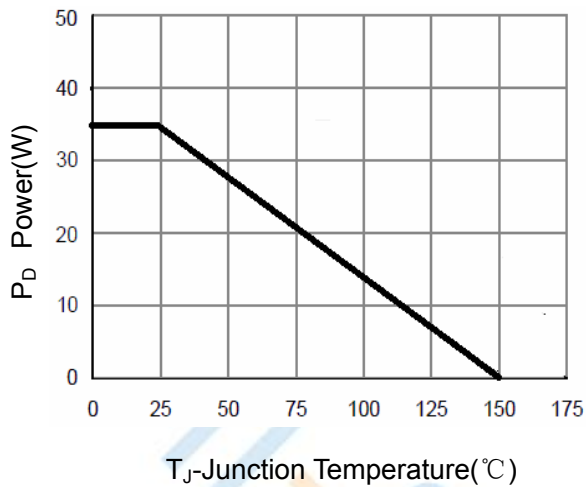


Figure 1 Switching Test Circuit

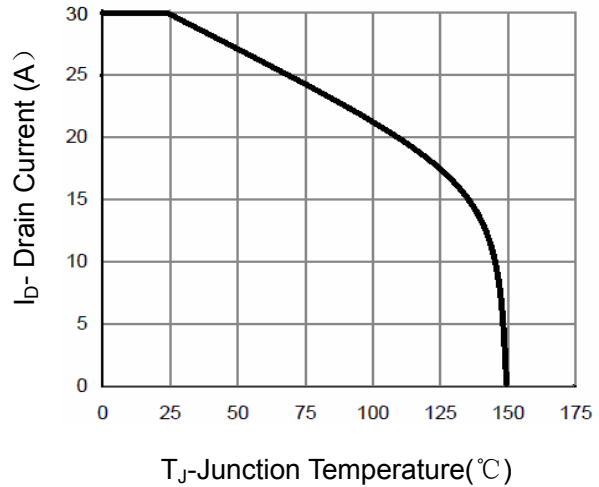


Figure 2 Switching Waveforms

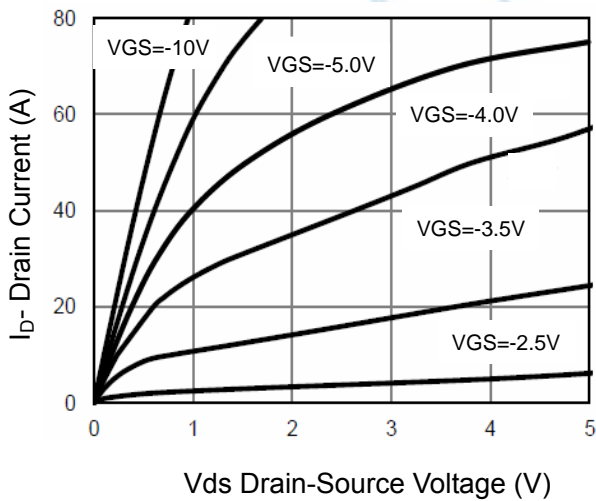


Figure 3 Power Dissipation

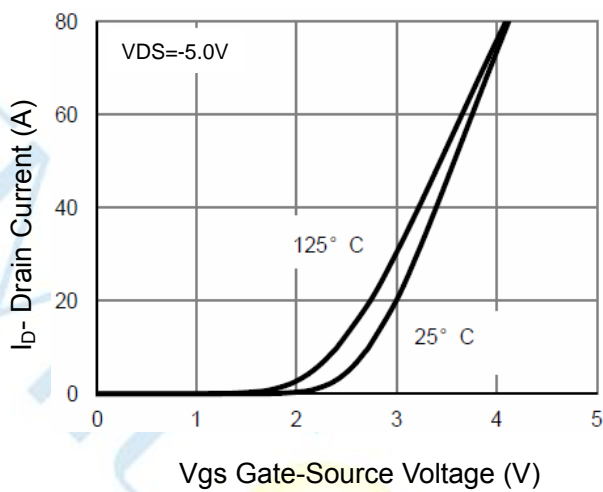


Figure 4 Transfer Characteristics

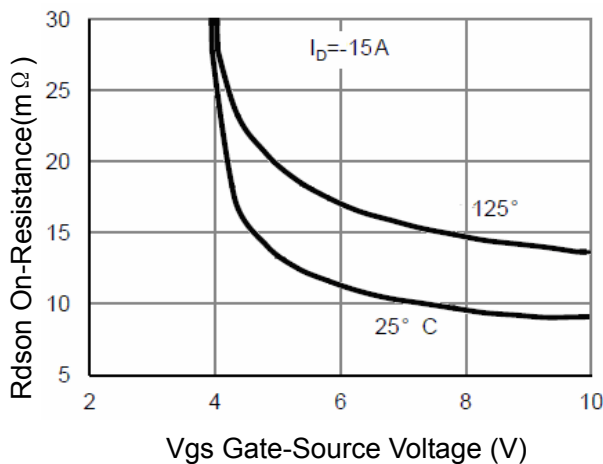


Figure 5 Rds(on) vs Vgs

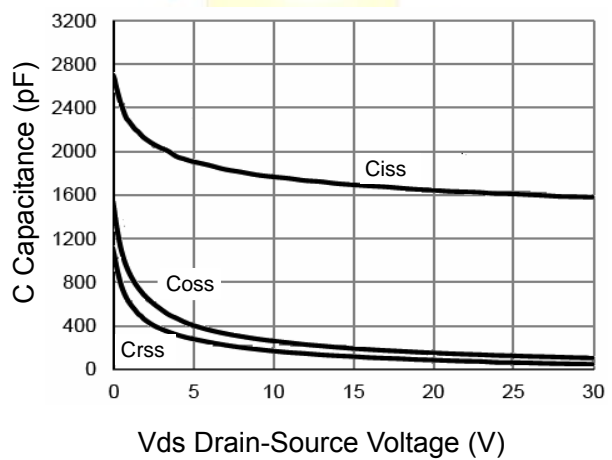


Figure 6 Capacitance vs Vds

P-Channel Typical Performance Characteristics

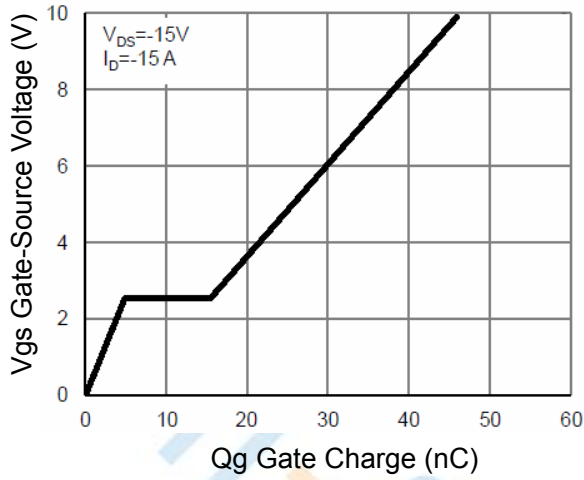


Figure 7 Gate Charge

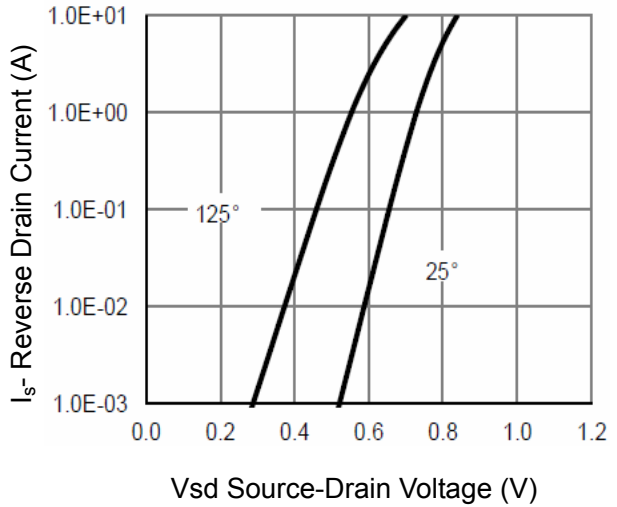


Figure 8 Source- Drain Diode Forward

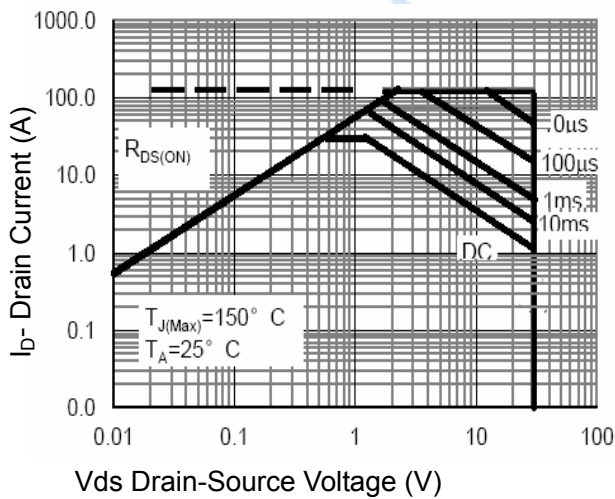


Figure 9 Safe Operation Area

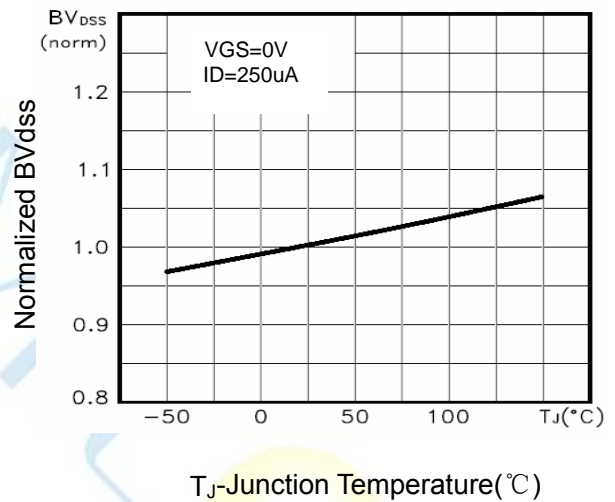


Figure 10 BV_{DSS} vs Junction Temperature

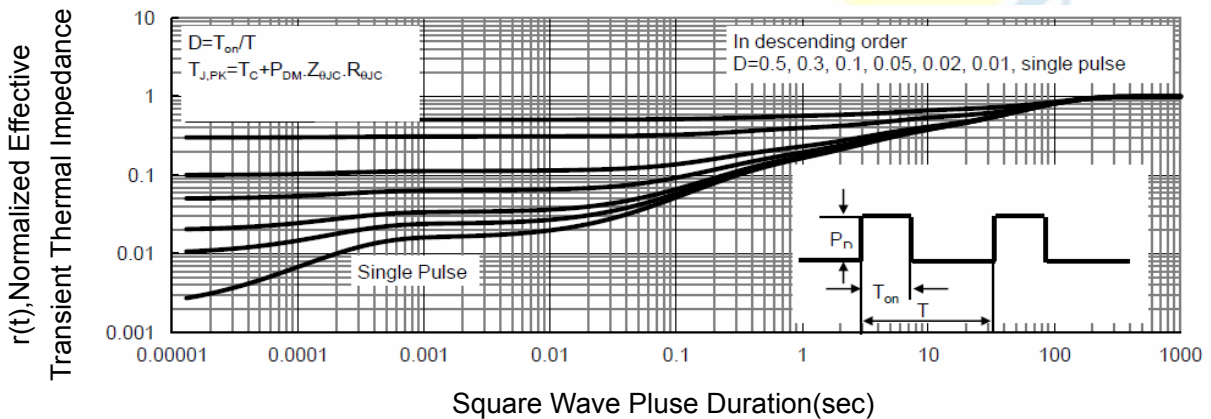
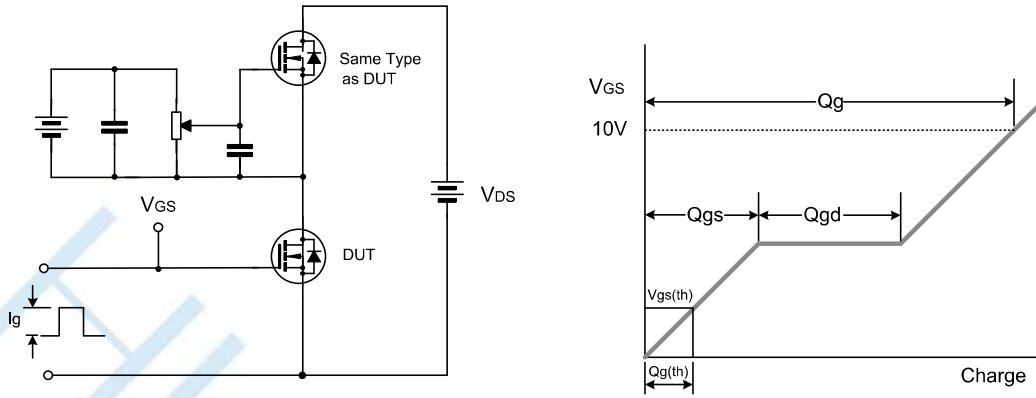
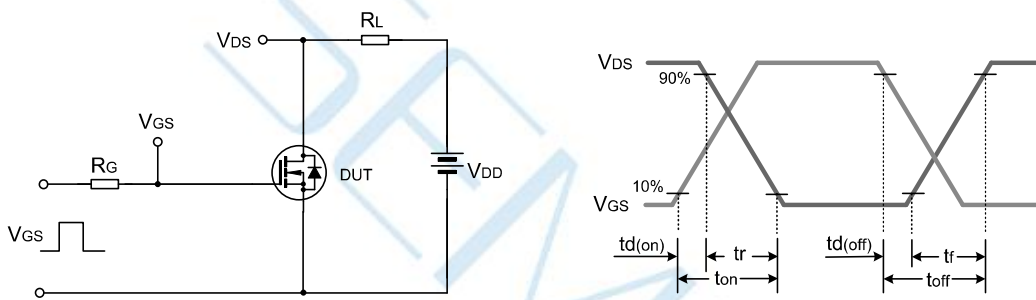


Figure 11 Normalized Maximum Transient Thermal Impedance

Test Circuit



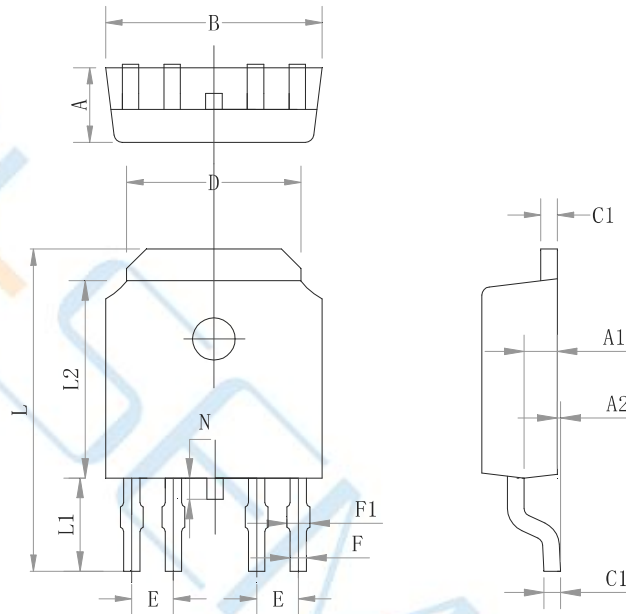
Gate Charge Test Circuit & Waveform



Resistive Switching Test Circuit & Waveform

Package Dimensions of TO-252-4L

Unit:mm



Symbol	Min	Typ	Max
A	2.22	2.30	2.38
A1	0.93	1.01	1.08
A2	0.05	0.15	0.20
B	6.52	6.60	6.68
C	0.48	0.50	0.54
C1	0.48	0.50	0.54
D	5.22	5.32	5.42
E	1.27 TYP		
F	0.40	0.50	0.60
F1	0.50	0.60	0.70
L	9.77	9.97	10.17
L1	2.67	2.87	3.07
L2	6.02	6.10	6.18
N	0.55	0.65	0.75

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